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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Eliyahou Harari  
Title: Flash Memory Cell Arrays Having Dual Control Gates Per Memory Cell Charge Storage Element  
Application No.: 10/791,486 Filing Date: March 1, 2004  
Examiner: Unknown Group Art Unit: 2818  
Docket No.: SNDK.304US1 Conf. No.: 1878

Certificate of Mailing Under 37 CFR 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on May 27, 2004

Signature

*Mary S. Buggie*

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Dear Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, Applicant calls the documents listed on the enclosed Form PTO-1449 to the Examiner's attention in this patent application.

This application has a filing date after June 30, 2003. Copies of the U.S. Patents and U.S. Published Patent Application documents listed on the accompanying Form PTO-1449 are not enclosed.

Citation of these documents shall not be construed as (1) an admission that the documents are prior art with respect to the invention or inventions claimed in this application, (2) a representation that a search has been made (other than as indicated by any cited document), or

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(3) an admission that the cited information is, or is considered to be, material to patentability as defined in § 1.56(b).

This information disclosure statement is submitted under 37 C.F.R. § 1.97(b) and consequently no fee should be required. The Commissioner is authorized, however, to charge any fee that may be required, or to credit any overpayment, against Deposit Account No. 502664. This form is being submitted in duplicate.

Respectfully submitted,



Gerald P. Parsons  
Reg. No. 24,486

May 27, 2004

Date

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U.S. Department of Commerce, Patent and Trademark				Atty. Docket No.		Application No.	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				SNDK.304US1		10/791,486	
				Applicant		Conf. No.	
(Use several sheets if necessary)				Eliyahou Harari		1878	
				Filing Date		Group	
				March 1, 2004		2818	

  

U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	1	5,736,443	Apr. 7, 1998	Park et al.			
	2	6,133,098	Oct. 17, 2000	Ogura et al.			

  

U.S. Published Patent Application Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	3	2002/0102774	Aug. 1, 2002	Kao et al.			

  

Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	4	1265289 A2	Dec. 11, 2002	Europe			X	
	5	1289023 A2	Mar. 5, 2003	Europe			X	
	6	0373698 A2	Dec. 6, 1989	Europe			X	
	7	06037328	Feb. 10, 1994	Japan			Abstract	X

  

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
8	"Twin MONOS Cell with Dual Control Gates" by Yutaka Hayashi et al., 2000 Symposium on VLSI Technology Digest of Technical Papers, pp. 122-123.	
9	"Notification of Transmittal of the International Search Report or the Declaration", corresponding PCT application no. PCT/US03/32383, International Searching Authority, European Patent Office, 24/03/2004, 9 pages.	

  

Examiner	Date Considered
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.